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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:  
Bryan D. Marietta et al.

Application No.: 09/758,864

Filed: January 11, 2001

Title: METHOD AND APPARATUS FOR  
MAINTAINING PACKET ORDERING  
WITH ERROR RECOVERY AMONG  
MULTIPLE OUTSTANDING PACKETS  
BETWEEN TWO DEVICES

) Patent No.: 6,862,283 B2

) Issue Date: March 1, 2005

) Examiner: Brian Nguyen

) Group Art Unit: 2661

) Docket No: SC11054TS-2

Commissioner for Patents  
Alexandria, VA 22313-1450

SUBMISSION OF CERTIFICATE OF CORRECTION

Dear Commissioner:

Enclosed, in duplicate, is a Certificate of Correction listing four errors in the subject patent. The Applicant erred with respect to the first error and the U.S.P.T.O. erred with respect to the other errors. Please enter these corrections.

Applicant authorizes the Commissioner to debit Deposit Account 503079, Freescale Semiconductor, Inc. the Applicant for any fees due.

Respectfully submitted,

April 18, 2005  
Date

Robert L. King  
Robert L. King  
Attorney for Applicant(s)  
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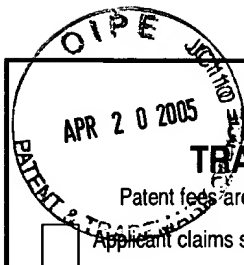
04/21/2005 DEMMANU2 00000143 503079 6862283  
01 FC:1811 100.00 DA

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelop addressed to: Assistant Commissioner for Patents, Alexandria, VA 22313-1450 on:

4-18-05  
Date

Patricia Thomas  
Signature

Pat Thomas  
Printed or Typed Name



# FEE TRANSMITTAL

Patent fees are subject to annual revision

☐ Applicant claims small entity status. See 37 CFR 1.27

## Complete if Known

Application Number 09/758,864  
Filing Date January 11, 2004  
First Named Inventor Bryan D. Marietta et al.  
Examiner Name Brian Nguyen  
Group Art Unit 2661  
Attorney Docket No. SC11054TS-2

TOTAL AMOUNT OF PAYMENT

(\$ 100

## METHOD OF PAYMENT (check all that apply)

☐ Check ☐ Credit card ☐ Money Order ☐ Other ☐ None

☒ Deposit Account:

Deposit Account Number

503079

Deposit Account Name

FREESCALE  
SEMICONDUCTOR, INC.

The Director is authorized to: (check all that apply)

☒ Charge fee(s) indicated below ☒ Credit any overpayments

☒ Charge any additional fee(s) during the pendency of this application

☐ Charge fees(s) indicated below, except for the filing fee to the above-identified deposit account.

## FEE CALCULATION (continued)

### 3. ADDITIONAL FEES

Large Entity	Small Entity	Fee	Fee	Fee	Fee	Fee Description
Code	Code	(\$)	Code	Code	(\$)	
1051	130	2051	65			Surcharge - late filing fee or oath
1052	50	2052	25			Surcharge - late Provisional filing
1053	130	1053	130			Non-English specification
1812	2520	1812	2520			For filing a request for ex parte Reexamination
1804	920*	1804	920*			Requesting publication of SIR prior to Examiner action
1805	1840*	1805	1840*			Requesting publication of SIR after Examiner action
1251	120	2251	60			Extension for reply within first month
1252	450	2252	225			Extension for reply within second month
1253	1020	2253	510			Extension for reply within third month
1254	1590	2254	795			Extension for reply within fourth month
1255	2160	2255	1080			Extension for reply within fifth month
1401	500	2401	250			Notice of Appeal
1402	500	2402	250			Filing a brief in support of an appeal
1403	1000	2403	500			Request for oral hearing
1451	1510	1451	1510			Petition to institute a public use proceeding
1452	110	2452	55			Petition to revive - unavoidable
1453	1370	2453	685			Petition to revive - unintentional
1501	1400	2501	700			Utility issue fee (or reissue)
1502	800	2502	400			Design issue fee
1503	1100	2503	550			Plant issue fee
1460	130	1460	130			Petitions to the Commissioner
1807	130	1807	130			Processing fee under 37 CFR 1.17(q)
1806	180	1806	180			Submission of IDS
8021	40	8021	40			Recording each patent assignment per property (times number of properties)
1809	790	2809	395			Filing a submission after final rejection (37 CFR § 1.129(a))
1810	790	2810	395			For each additional invention to be examined (37 CFR § 1.129(b))
1801	790	2801	395			Request for Continued Examination (RCE)
1802	900	1802	900			Request for expedited examination of a design application
Other fee (specify)						1811 Certificate of Correction \$100

## FEE CALCULATION

### 1. BASIC FILING FEE

Large Entity	Small Entity	Fee	Fee	Fee	Fee	Fee Description	Fee Paid
Code	Code	(\$)	Code	Code	(\$)		
1011	300	2011	150			Utility filing fee	300
1111	500	2111	250			Utility search fee	500
1311	200	2311	100			Utility Exam fee	200
1002	300	2002	175			Design filing fee	
1003	550	2003	275			Plant filing fee	
1004	790	2004	395			Reissue filing fee	
1005	200	2005	100			Provisional filing fee	

SUBTOTAL (1) (\$)

### 2. EXTRA CLAIM FEES

Total Claims	Previously Paid**	Extra Claims	Fee from below	Fee Paid
	20		50	
Independent Claims	3		200	

Multiple Dependent

360 =

Large Entity	Small Entity	Fee	Fee	Fee	Fee	Fee Description
Code	Code	(\$)	Code	Code	(\$)	
1202	50	2202	25			Claims in excess of 20
1201	200	2201	100			Independent claims in excess of 3
1203	360	2203	180			Multiple dependent claim, if not paid
1204	88	2204	44			* Reissue independent claims over original patent
1205	200	2205	100			* Reissue claim s in excess of 20 and over original patent

SUBTOTAL (2) (\$)

\*\*or number previously paid, if greater; For Reissues, see above.

SUBTOTAL (3) (\$ 100

\* Reduced by Basic Filing Fee Paid

## SUBMITTED BY

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Signature

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Date

April 18, 2005

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number.  
(Also Form PTO-1050)

UNITED STATES PATENT AND TRADEMARK OFFICE  
CERTIFICATE OF CORRECTION

PATENT NO.: 6,862,283 B2

DATE: March 01, 2005

INVENTOR(S): Bryan D. Marietta et al.

It is certified that errors appear in the above-identified patent and that said Letters Patent are hereby corrected as shown below:

In Section 73, Assignees, change "Mercury Computer Sytemc, Inc." to --Mercury Computer Systems, Inc--

In Claim 1, Column 9, Line 2, change "transfer" to --retransfer --

In Claim 1, Column 9, Line 3, change "tho" to --the--

In Claim 9, Column 10, Line 8, change "form" to --from--

MAILING ADDRESS OF SENDER;  
Freescale Semiconductor, Inc.  
Law Department  
7700 West Parmer Lane, PL02  
Austin, TX 78729

PATENT NO. 6,567,424 B1

No. of additional copies: 1